

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: )  
Shunpei YAMAZAKI et al. ) Group Art Unit: 2822  
Serial No. 09/837,877 ) Confirmation No. 8131  
Filed: April 19, 2001 ) Examiner: M. Lewis  
For: A SEMICONDUCTOR DEVICE )  
HAVING A PIXEL MATRIX ) May 26, 2004  
CIRCUIT THAT INCLUDES A )  
PIXEL TFT AND A STORAGE )  
CAPACITOR (Previously Amended)



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CERTIFICATE OF MAILING OR TRANSMISSION  
[37 CFR 1.8(a)]

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on May 26, 2004.

Signature: Deborah Movahhedi

Name: Deborah Movahhedi

AMENDMENT

Mail Stop **Box AF**  
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Sir:

In response to the final Office Action mailed December 26, 2003, please amend the above identified application as follows.